

**REMARKS**

Reconsideration of the application in light of the amendments and the following remarks is respectfully requested.

**Status of the Claims**

Claims 1-12 are pending. Claims 1 and 5 have been amended. No new matter has been added.

The Applicant appreciatively acknowledges the Examiner's allowance of claims 9-11.

**Rejection Under 35 U.S.C. § 102**

Claims 1-8 and 12 stand rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,542,604 to Blon et al. ("Blon").

The Examiner contends that Blon discloses a method for correcting for an echo signal component in a telecommunications device comprising the steps of sampling a transmitted signal across a sampling resistor, subtracting the sampled signal (through a subtractor) from a line signal to obtain a reconstructed received signal, sampling the transmitted signal across a first RC network echo compensation circuit to obtain a first echo compensation signal, where the first echo compensation signal and the reconstructed received signal are subtracted by providing them to a first circuit node. The Examiner contends that the first circuit node is pins HYB3 and HYB4 of Blon Figure 1.

The Applicant disagrees and submits that pins HYB3 and HYB4 are two separate circuit nodes which are the connection points of an off-chip RC-ladder that merely replicates the input impedance of a 15 kilo-ft AWG 26 cable, and is itself connected to other impedance replicating circuitry. (Blon, column 3, lines 54-58, and Figure 1.) Blon generates a replica echo signal by





If there are any other issues remaining which the Examiner believes could be resolved through a Supplemental Response or an Examiner's Amendment, the Examiner is respectfully requested to contact the undersigned at the telephone number indicated below.

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Respectfully submitted,

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